

FIG. 1

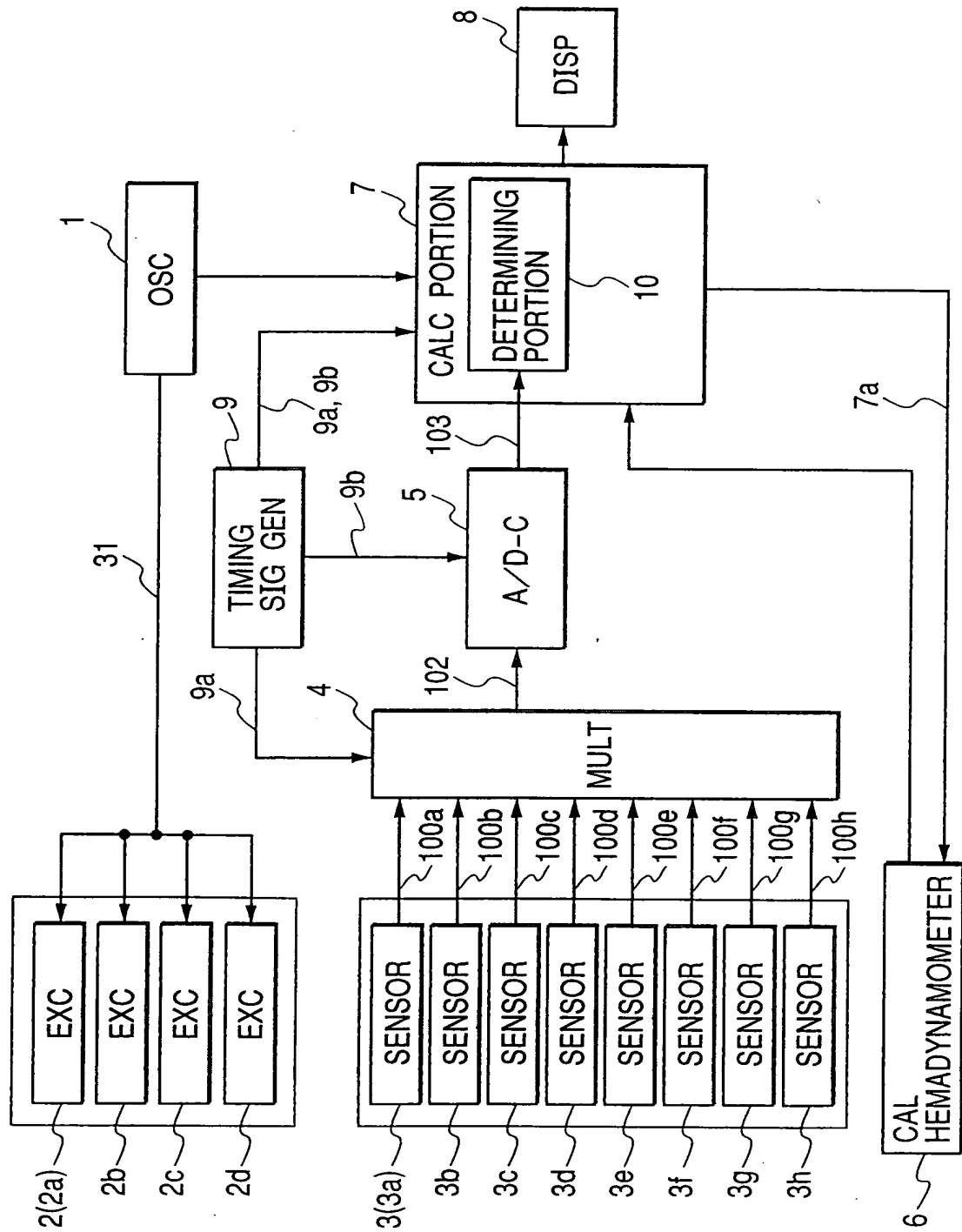


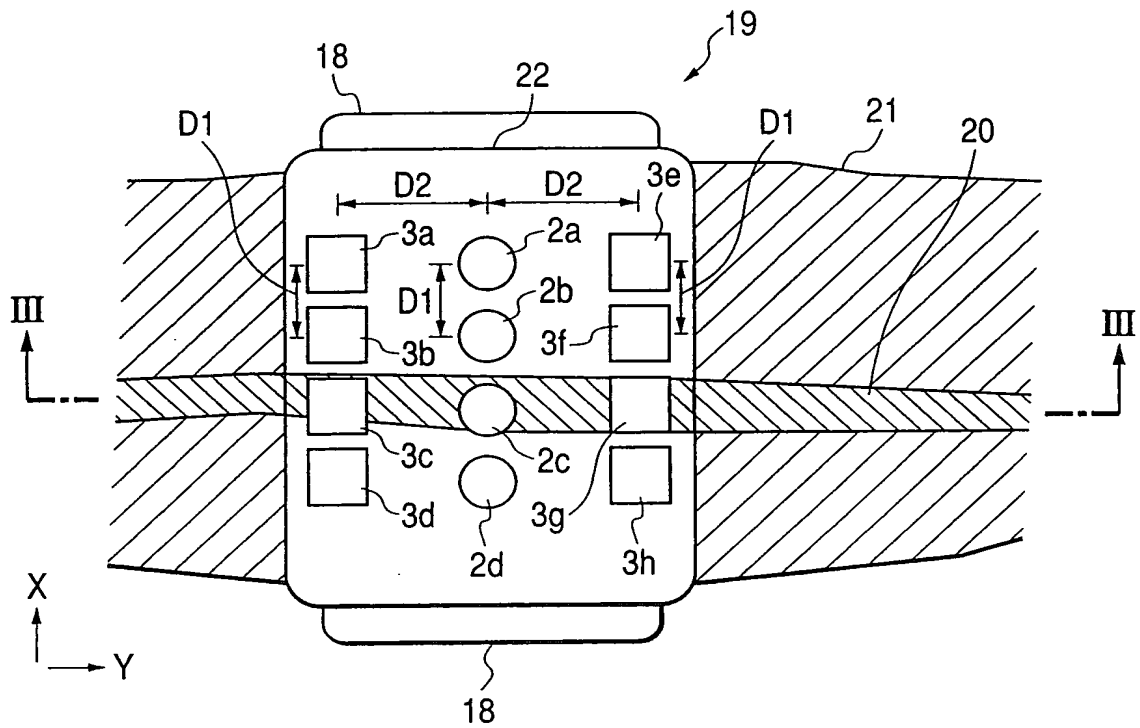
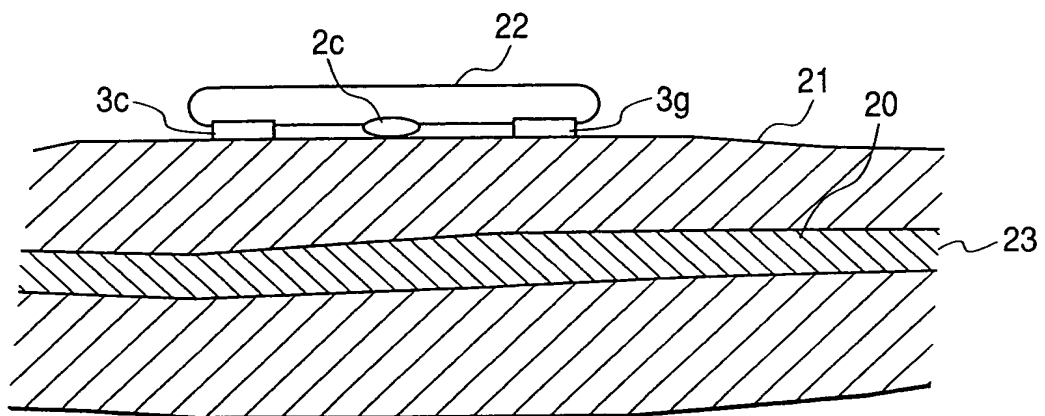
FIG. 2*FIG. 3*

FIG. 4A

DET SIG 100c

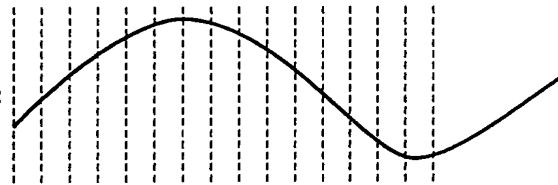


FIG. 4B

DET SIG 100d

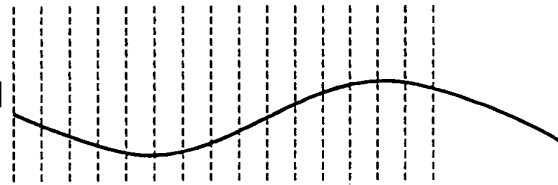


FIG. 4C

OUTPUT OF
MULT 4

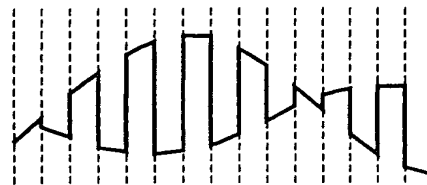


FIG. 4D

OUTPUT OF
A/D-C

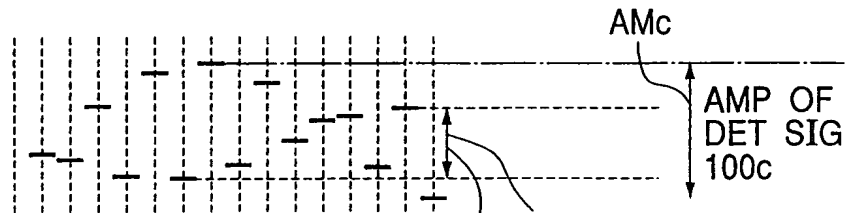


FIG. 4E

DETERMINED
DET SIG

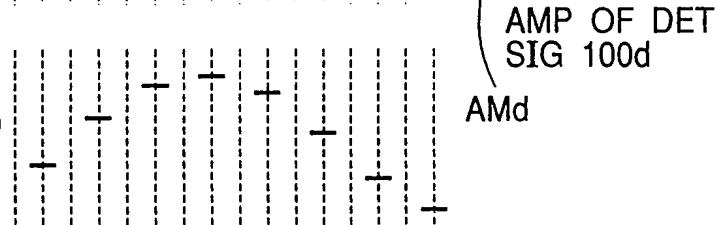


FIG. 5A

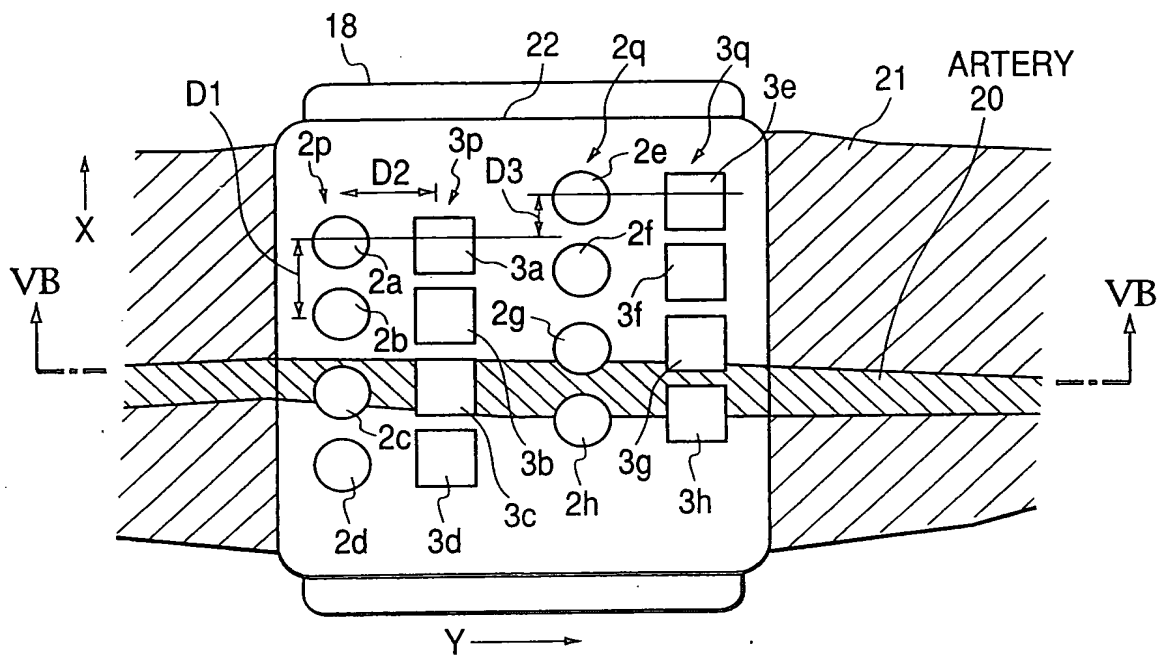


FIG. 5B

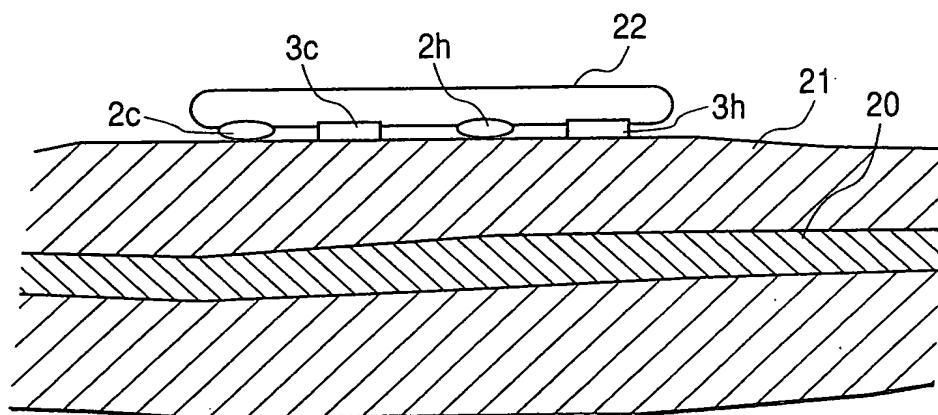


FIG. 6

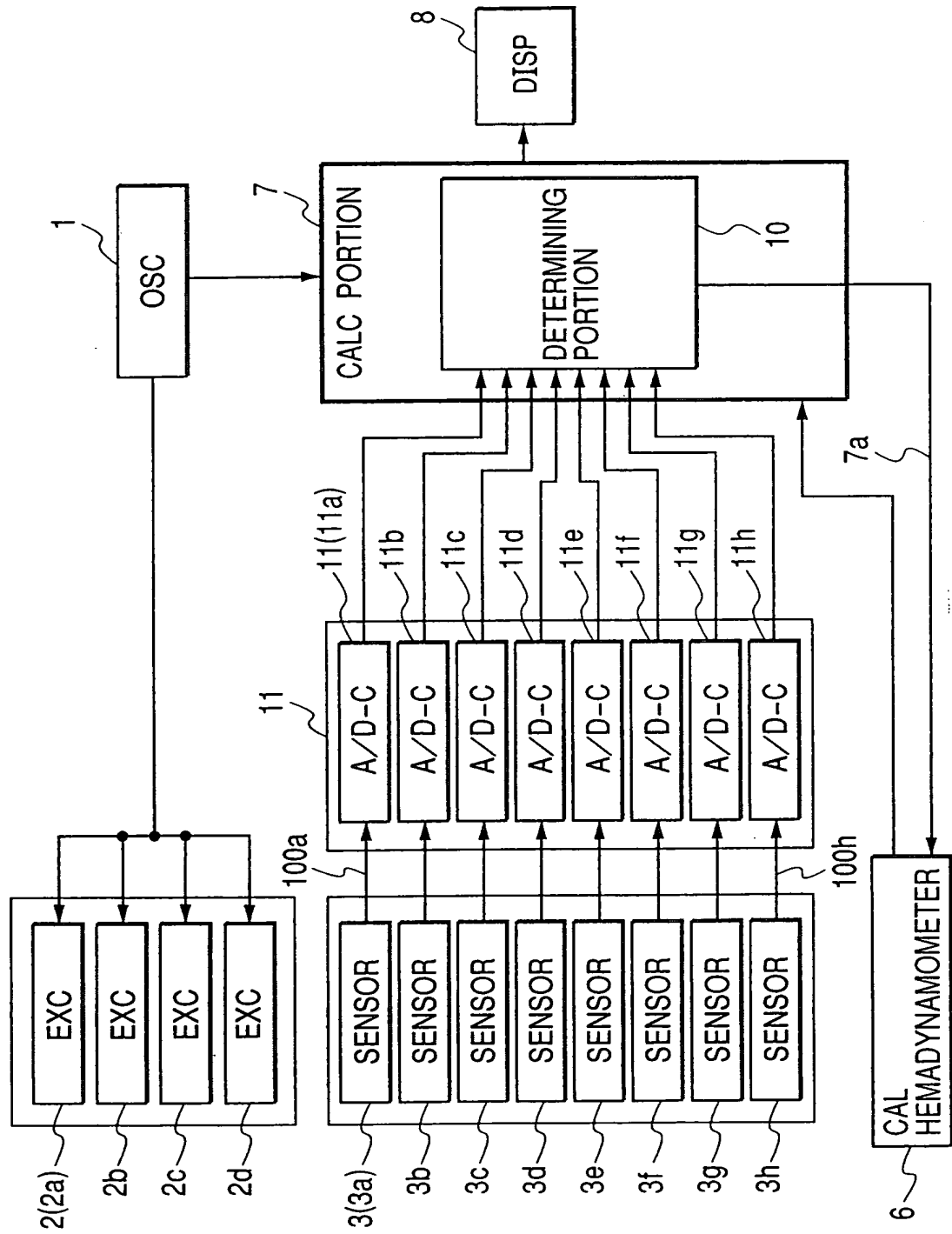


FIG. 7

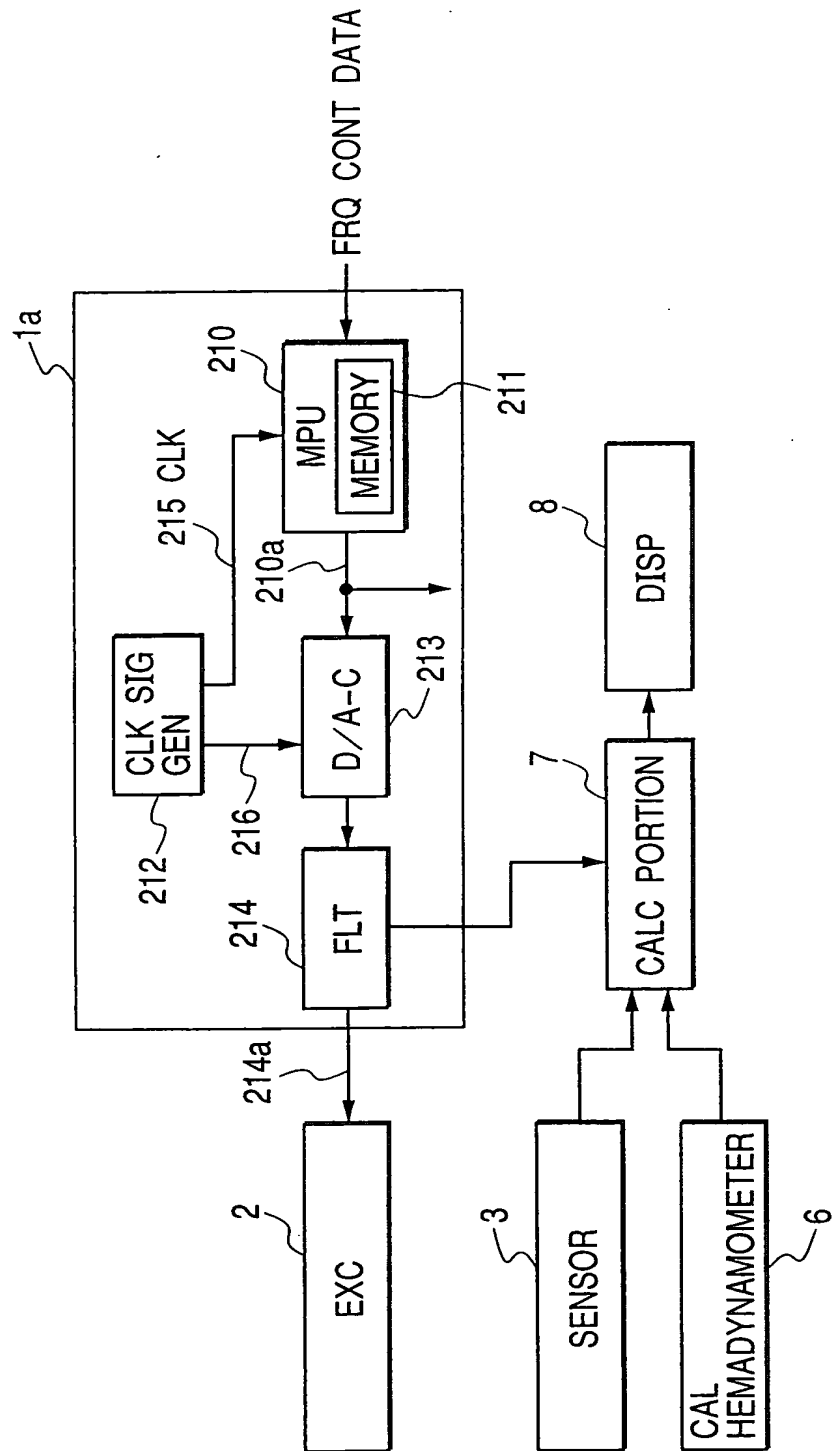


FIG. 8

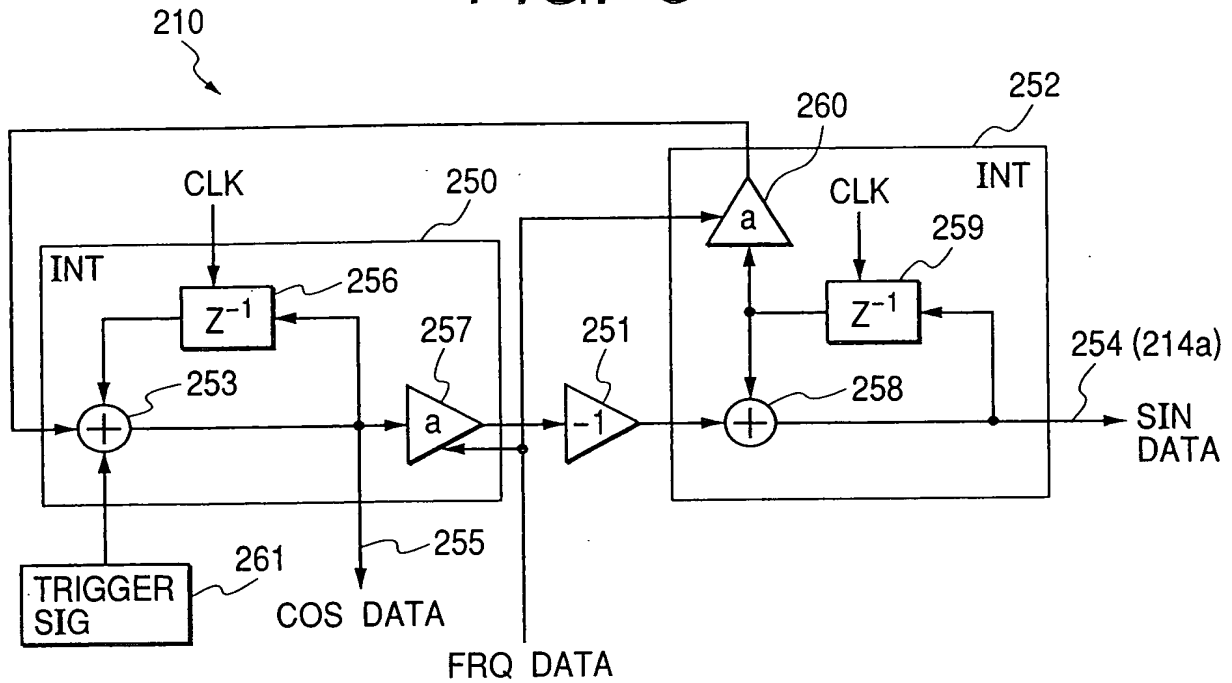


FIG. 9

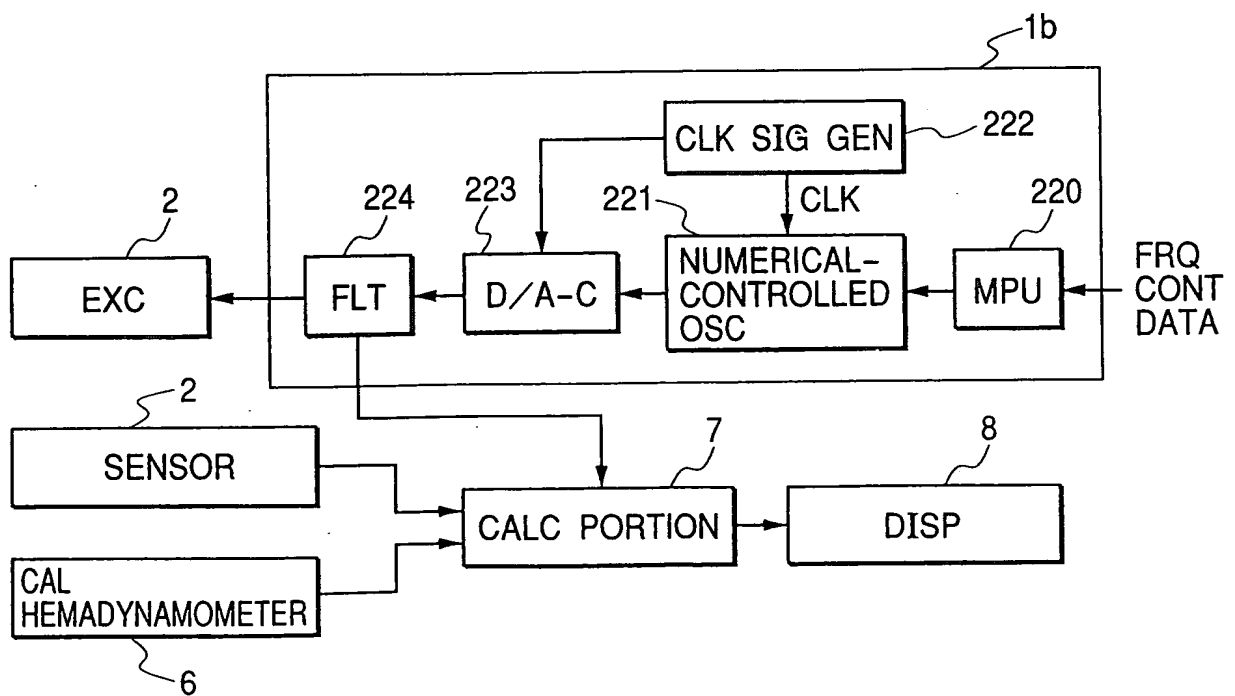


FIG. 10

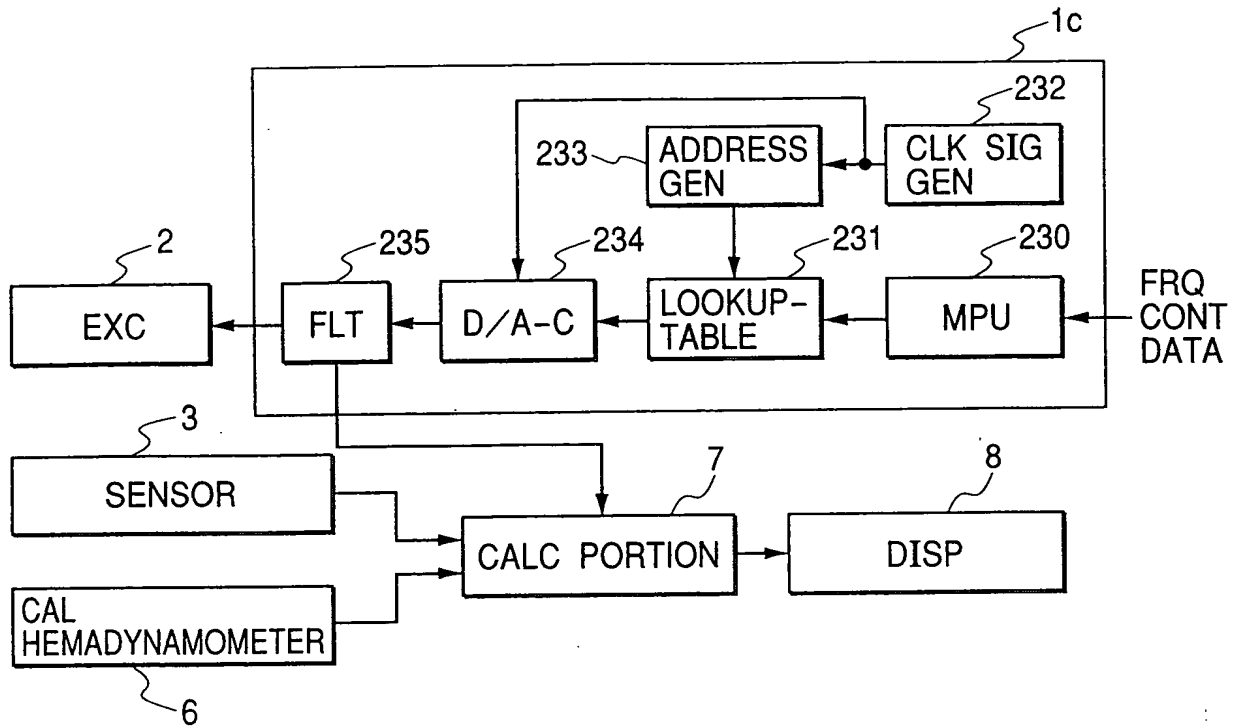


FIG. 11

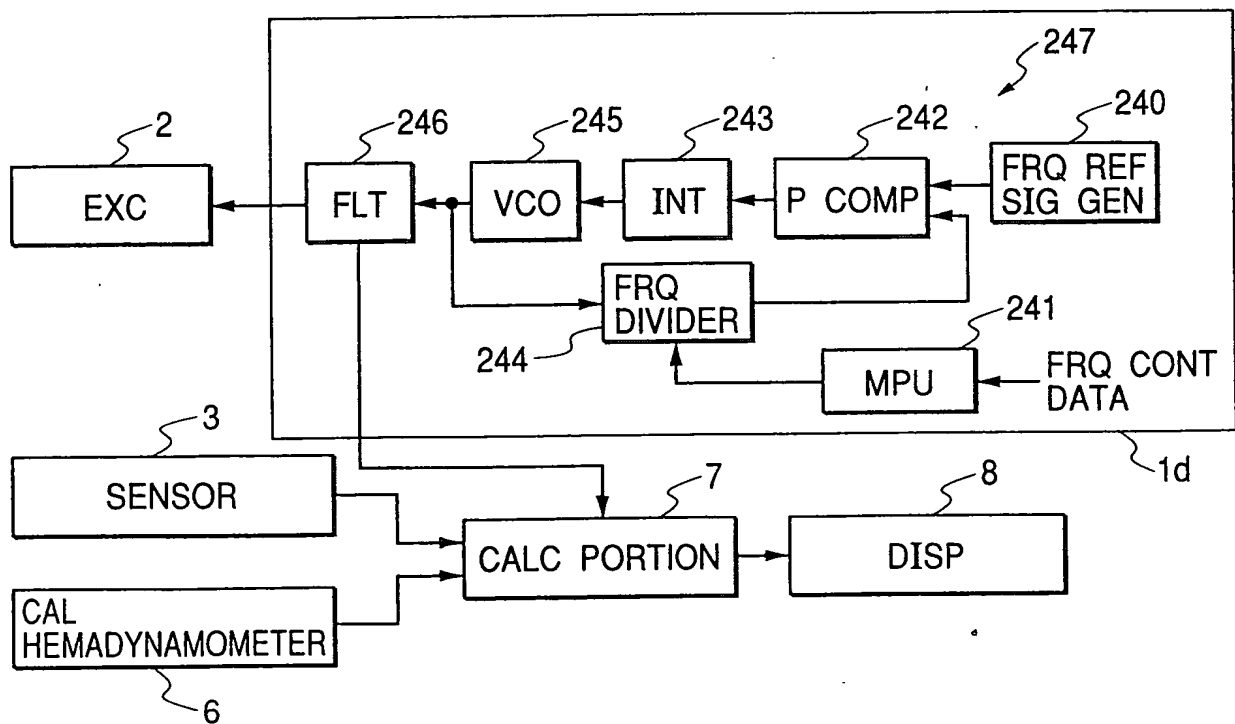


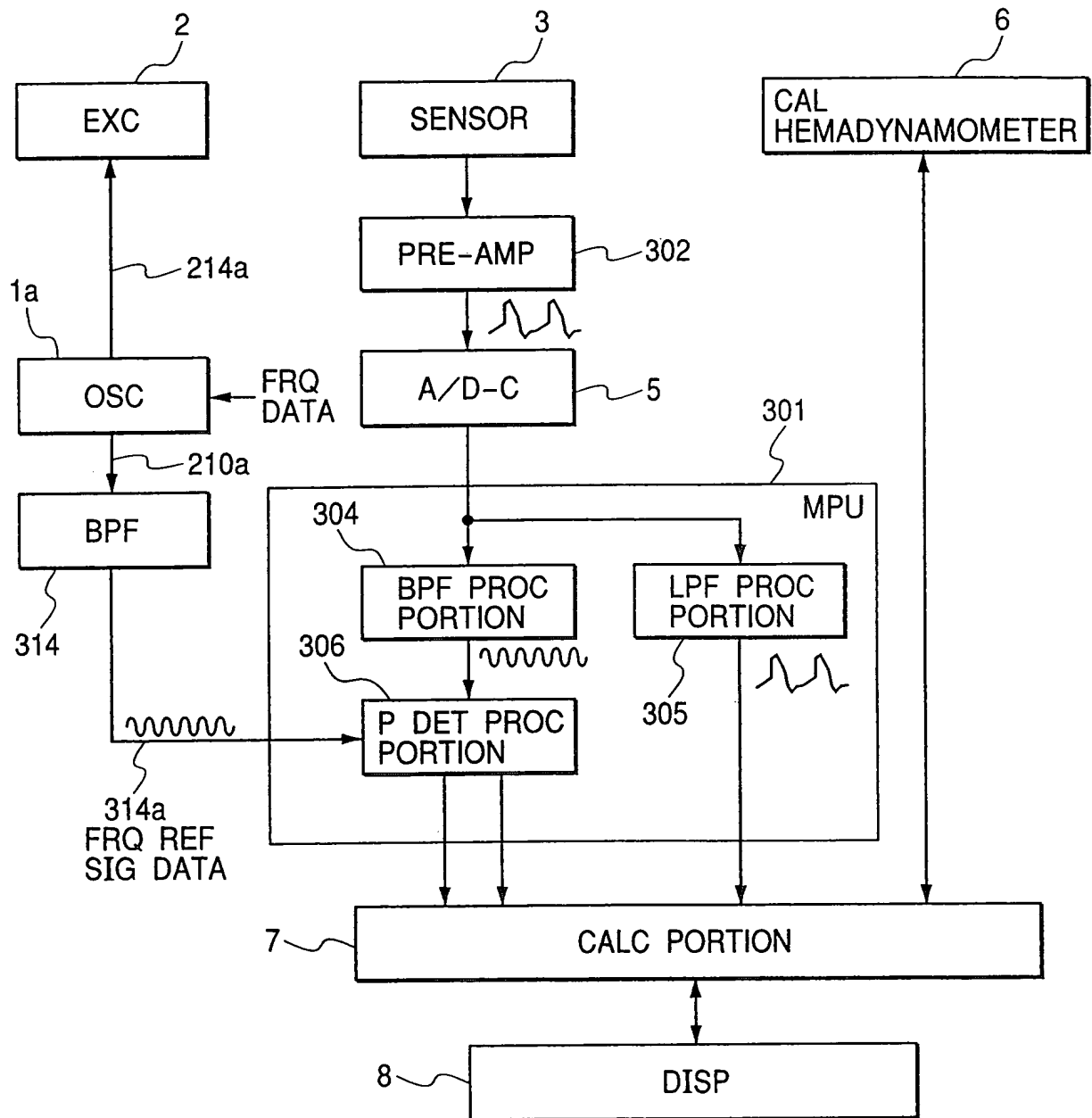
FIG. 12

FIG. 13A

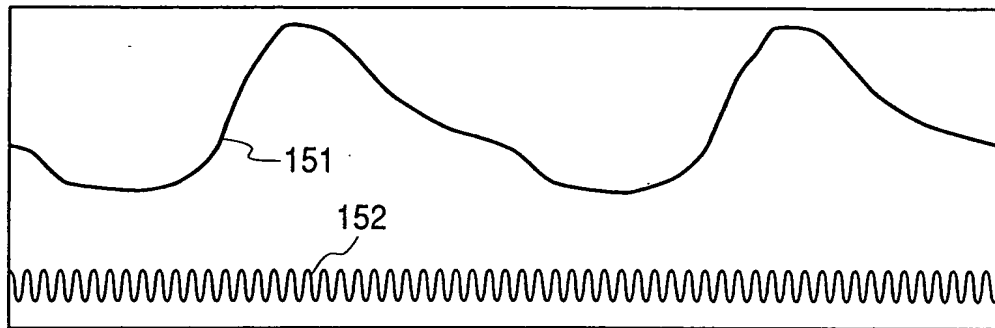


FIG. 13B

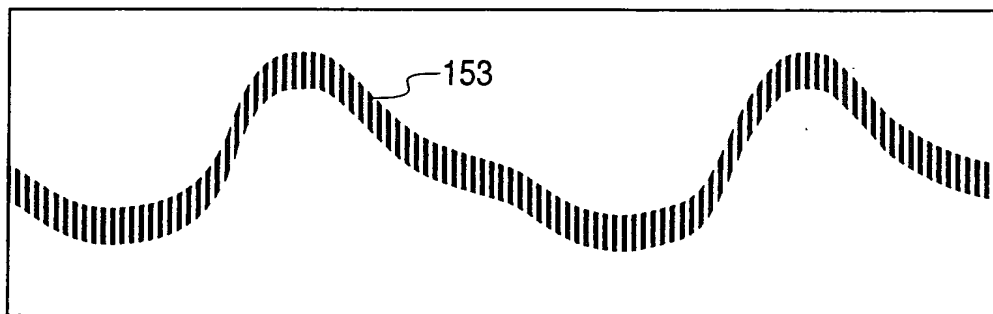


FIG. 14

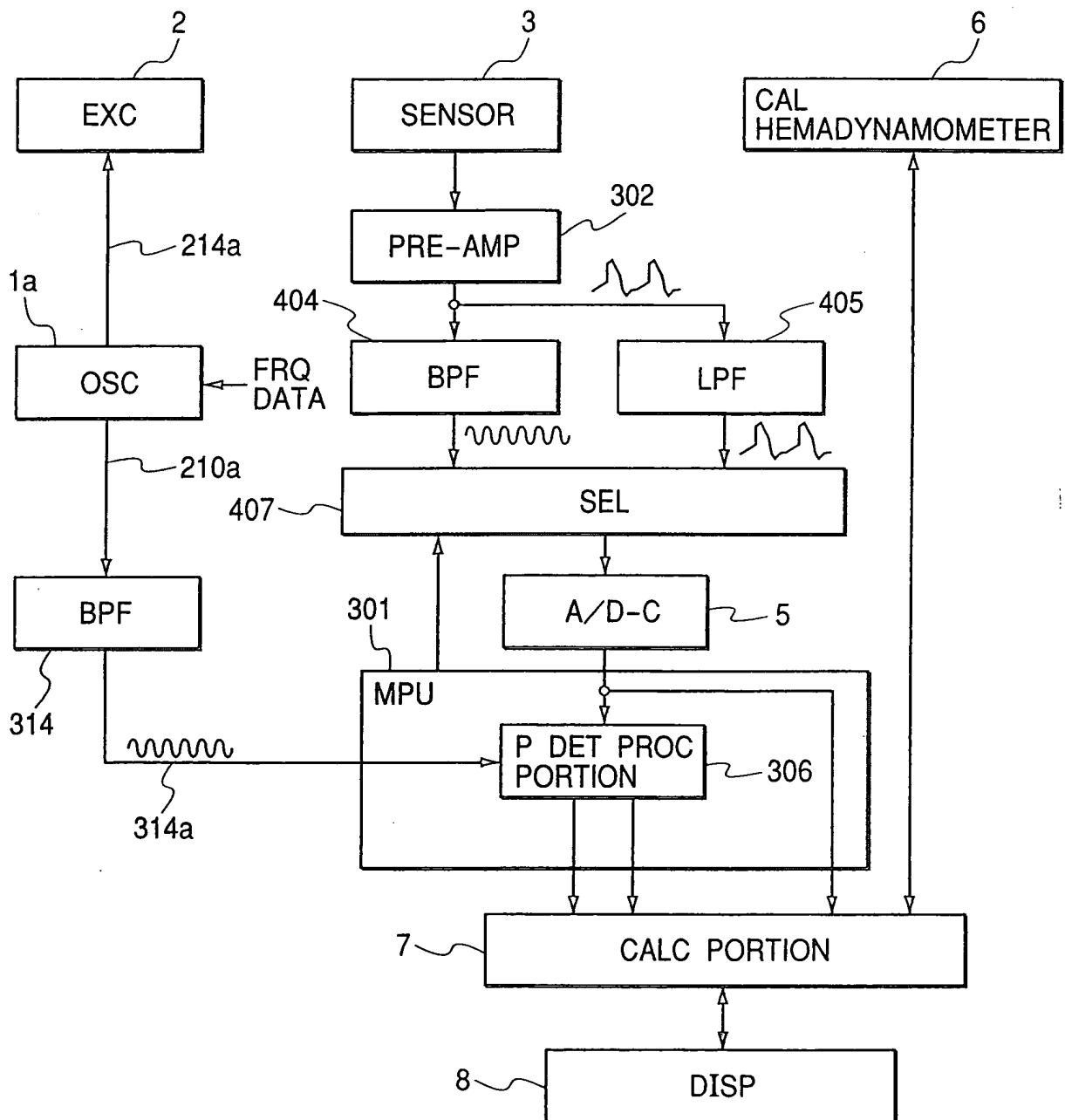


FIG. 15

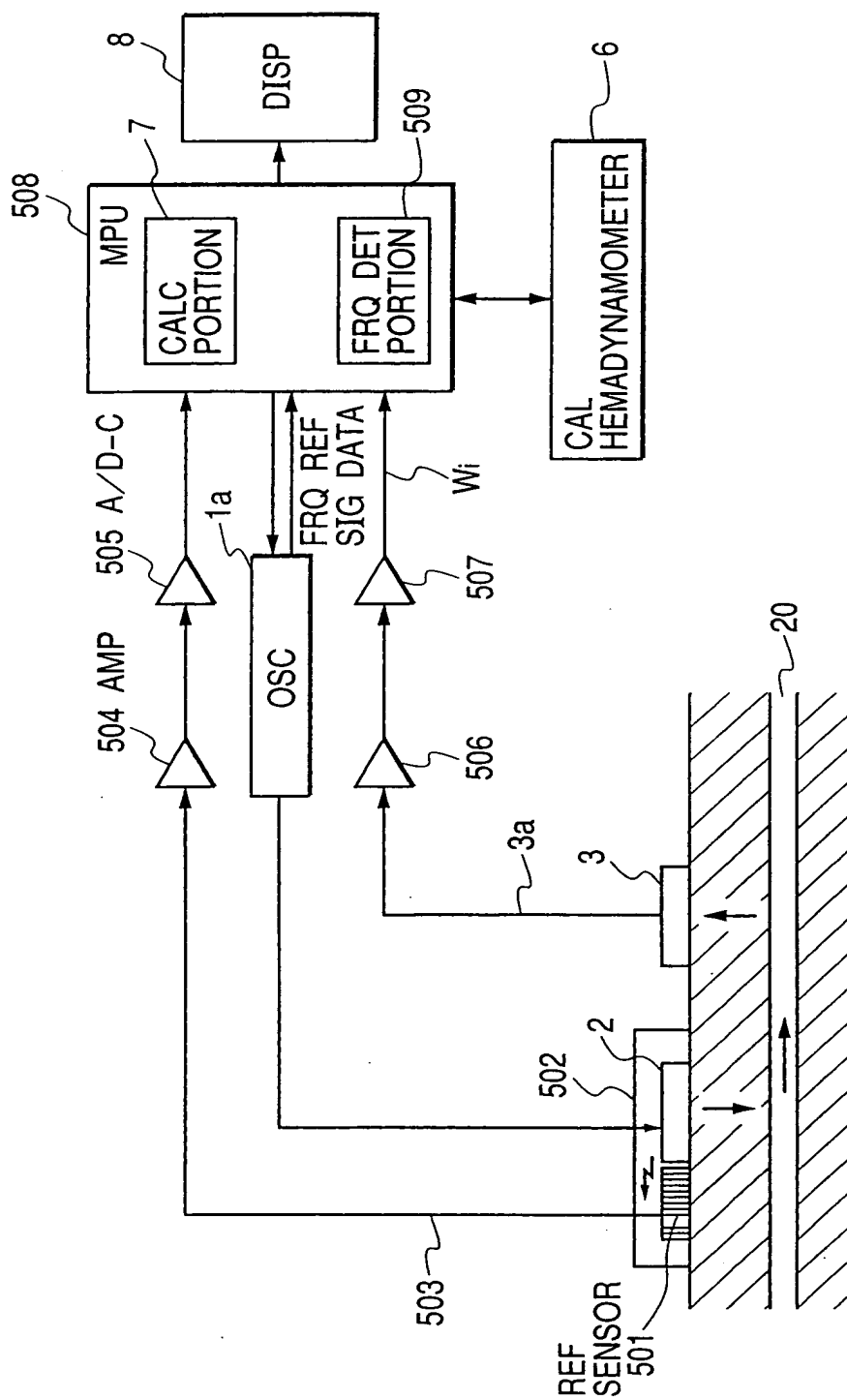


FIG. 16

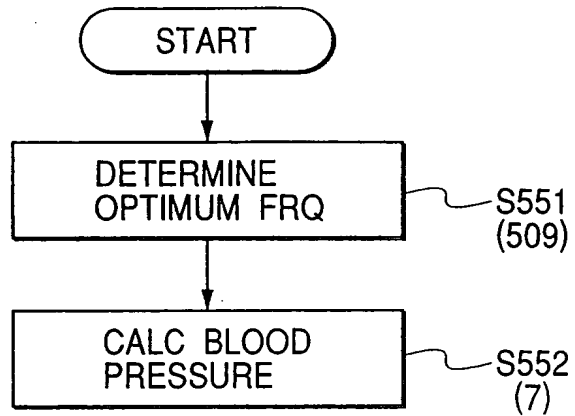


FIG. 18

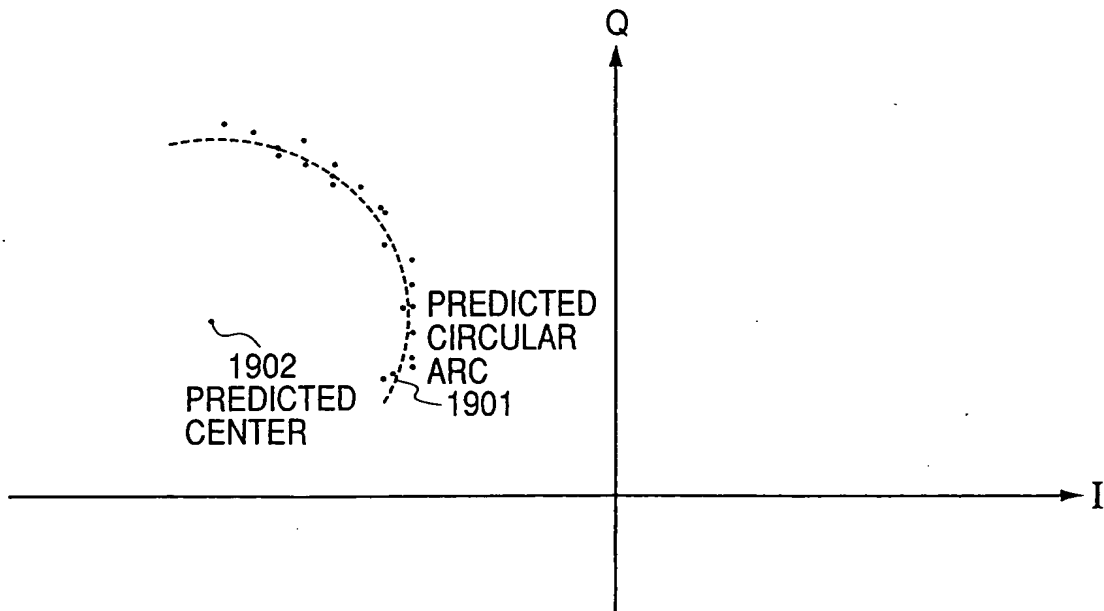


FIG. 17

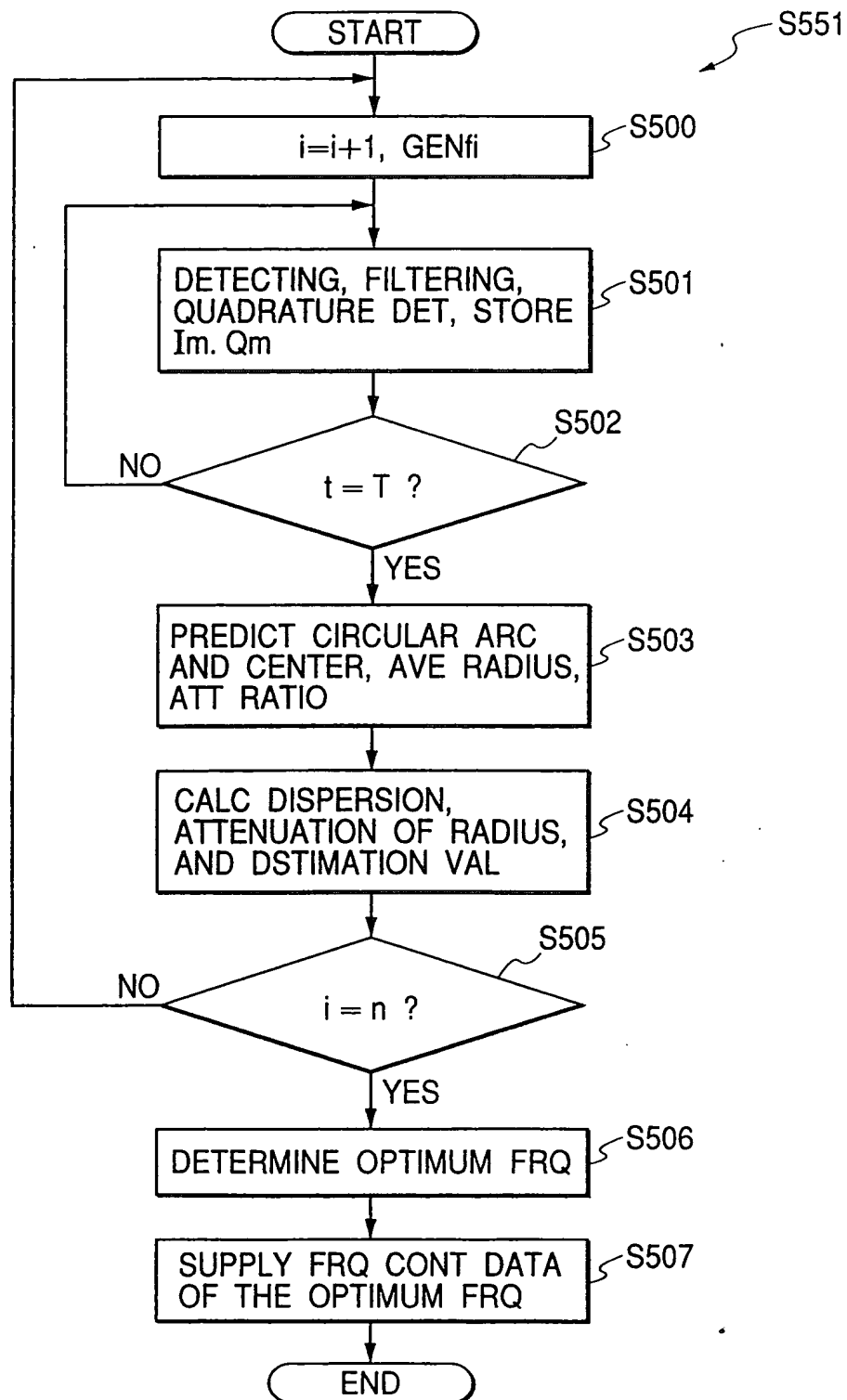


FIG. 20

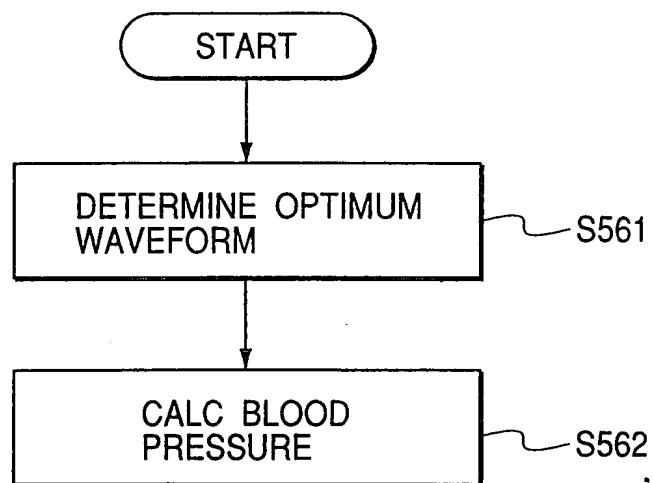


FIG. 21